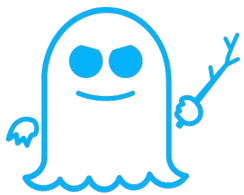


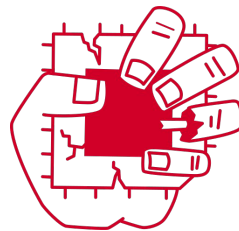
Když procesor netěsňuje

Michal Koutný, STG 2020-06-17



MELTDOWN

SPECTRE



Zombieload (MDS)



FORESHADOW

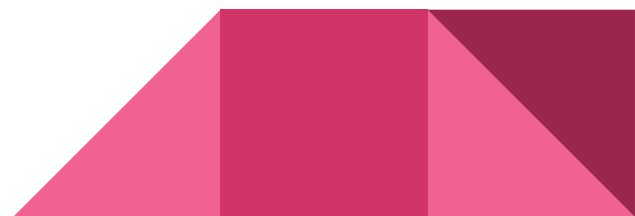
I/2018

VII/2018

V/2019

VI/2020

Crosstalk (SRBDS)

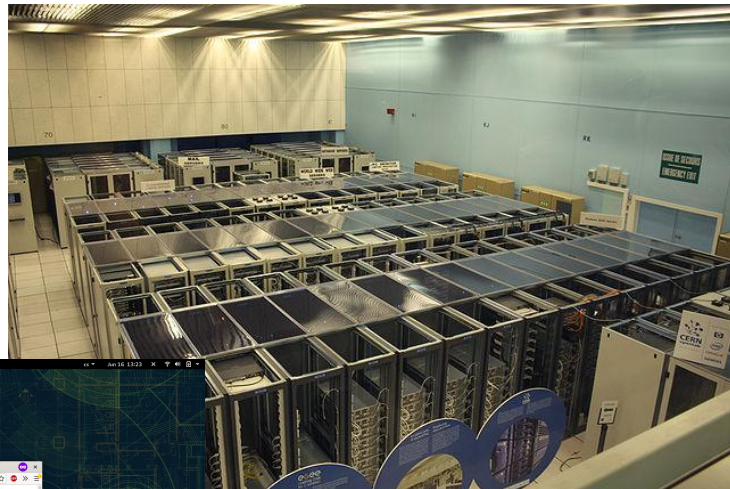


Bezpečnostní domény

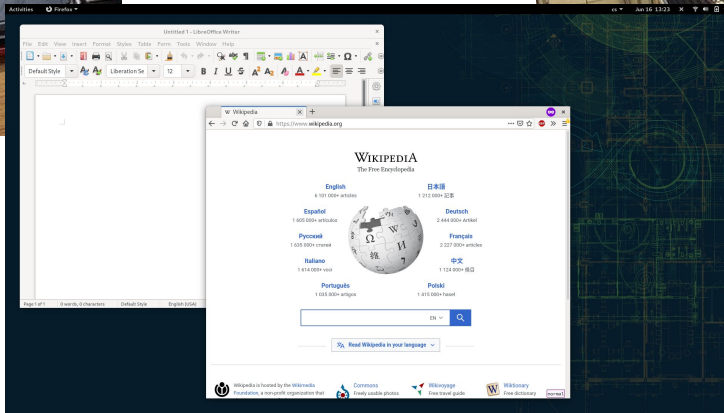


OS

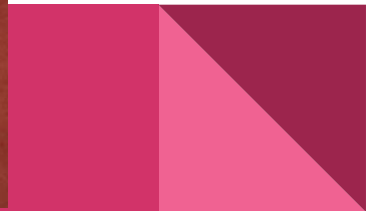
procesy



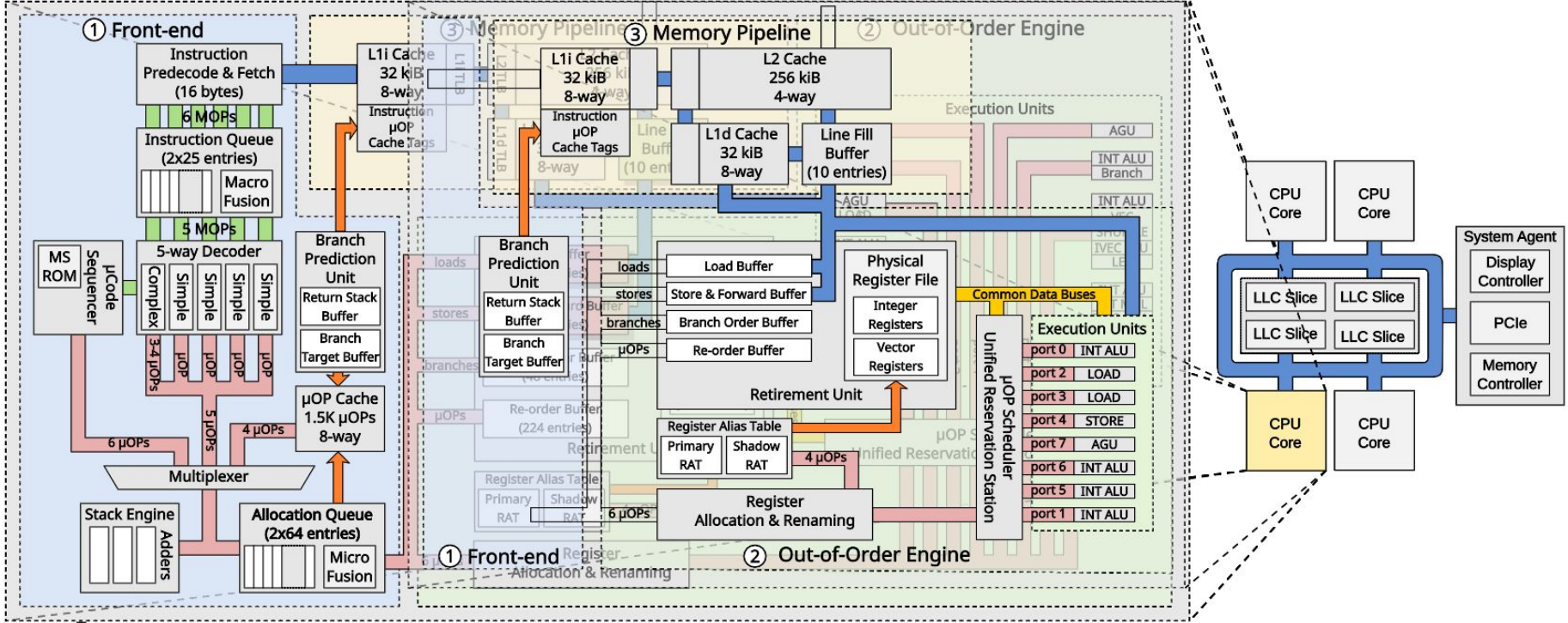
cloud/VM



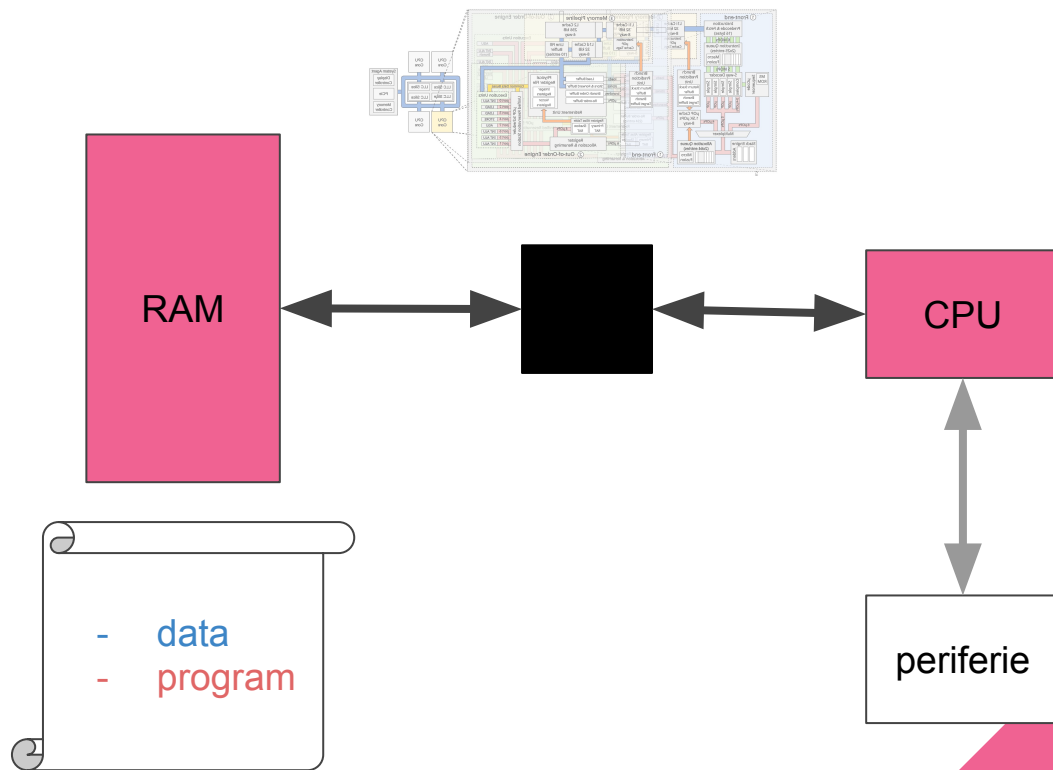
Které knihy jsou populární?



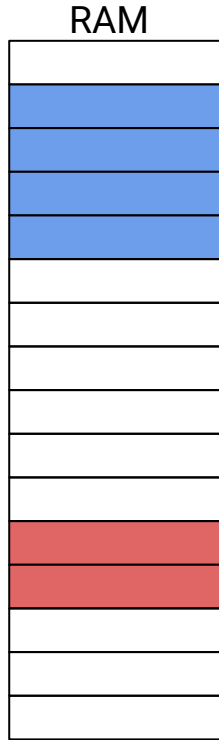
Architektura počítače*



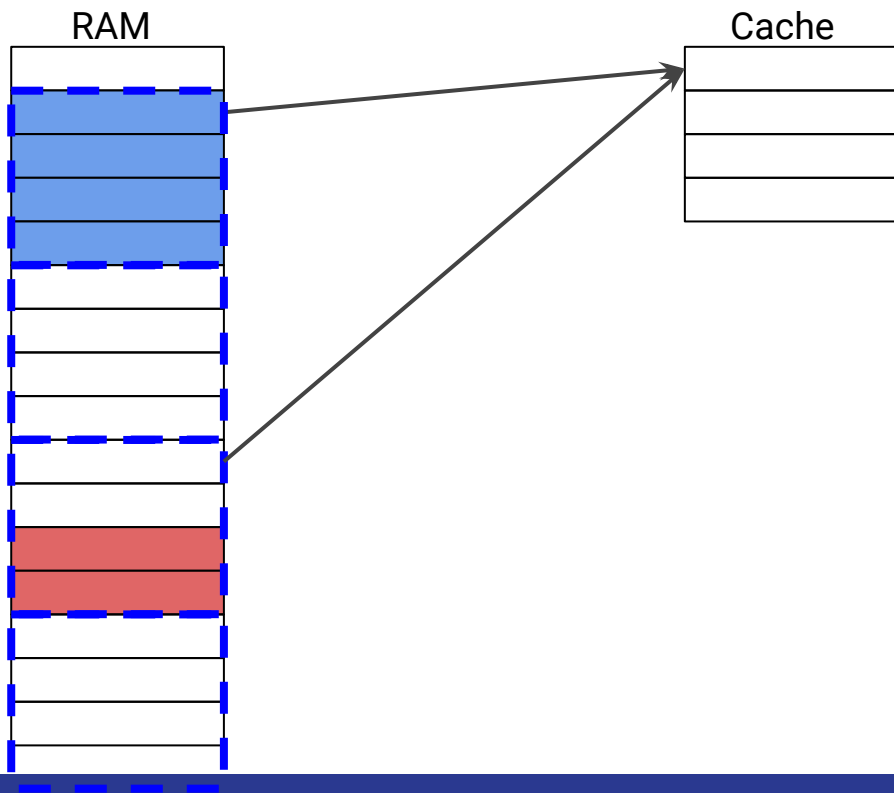
Architektura počítače*



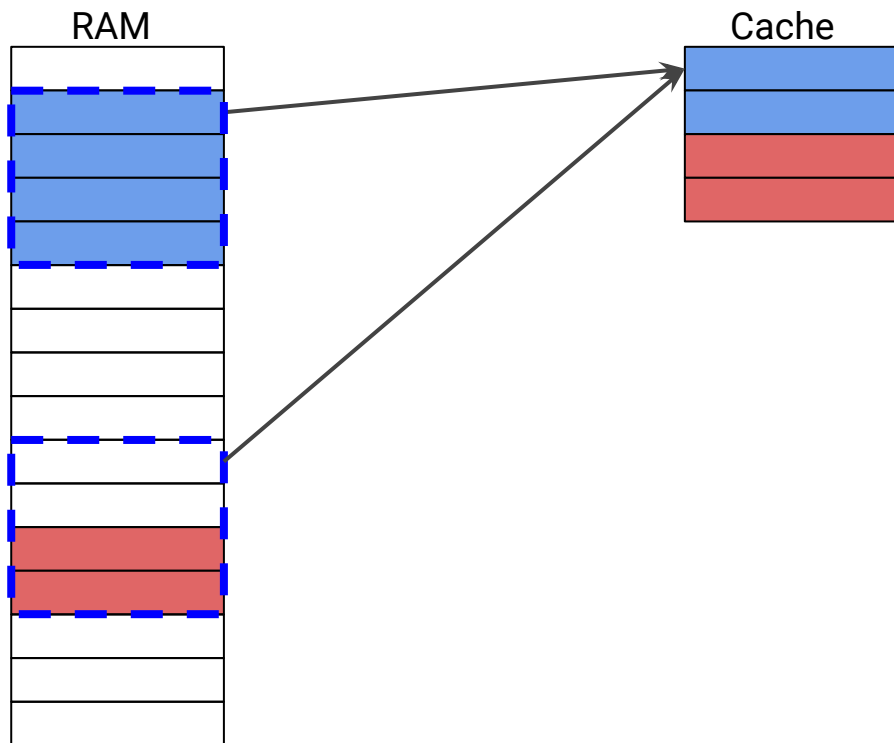
ASLR (address space layout randomization)



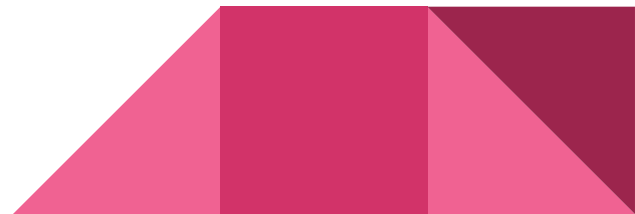
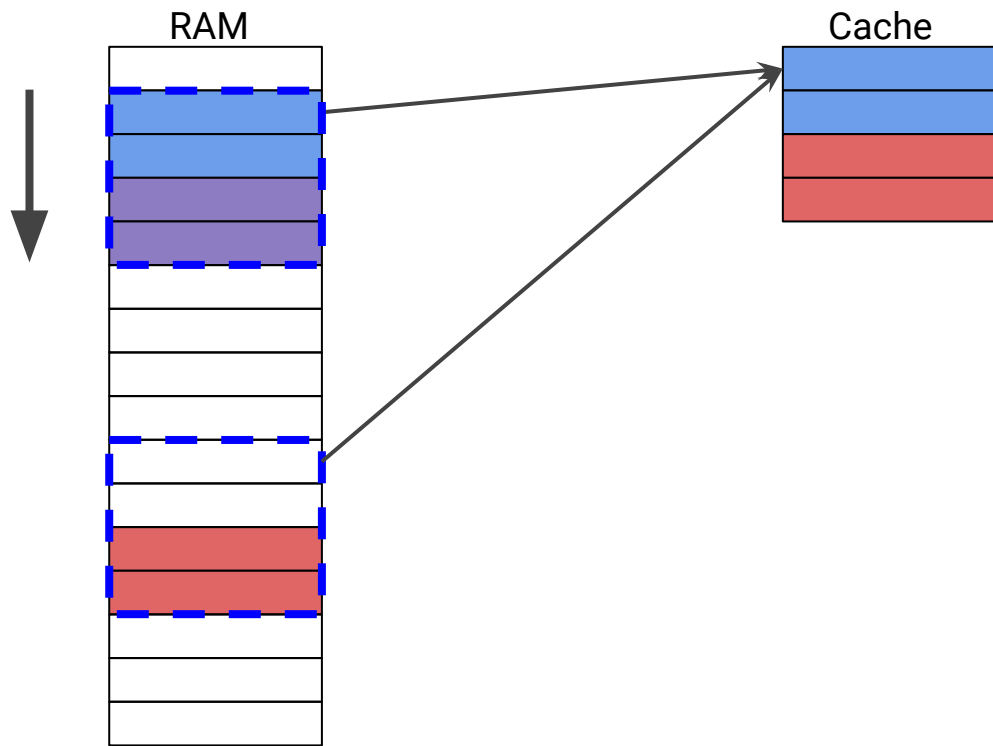
ASLR odhalení



ASLR odhalení

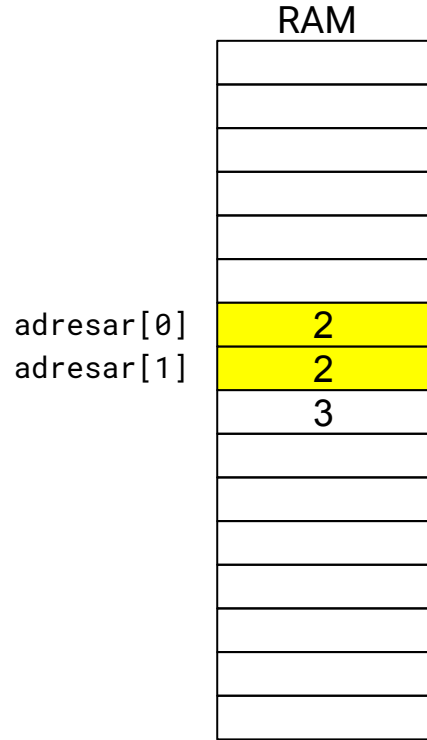


ASLR odhalení



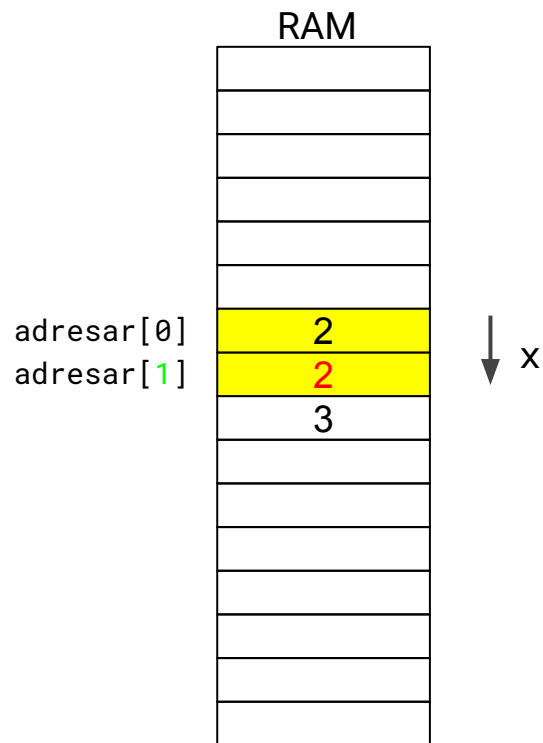
Víme kde

Spectre – úvod



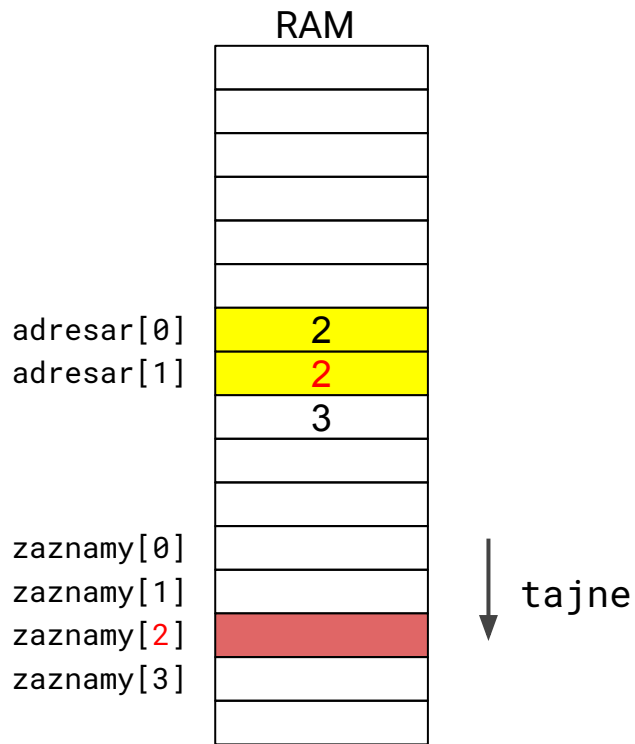
```
if (x < A)
    tajne = adresar[x]
    cti(zaznamy[tajne])
else
    mimo_adresar()
```

Spectre – úvod



```
if (x < A)
    tajne = adresar[x]
    cti(zaznamy[tajne])
else
    mimo_adresar()
```

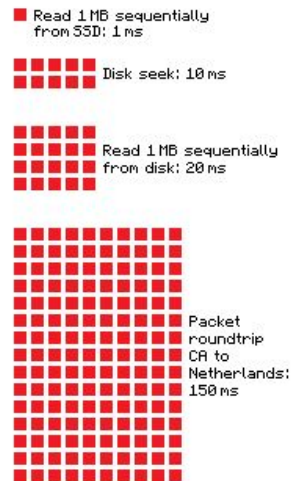
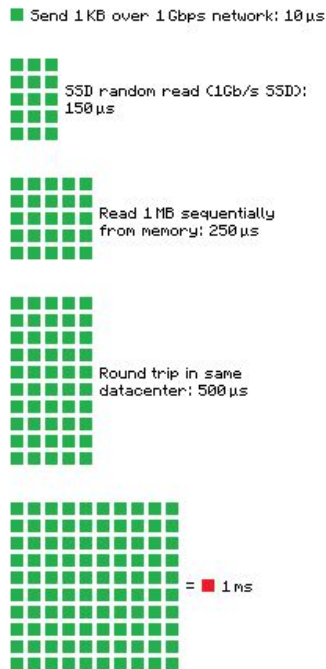
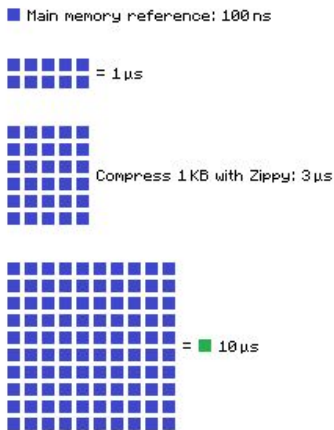
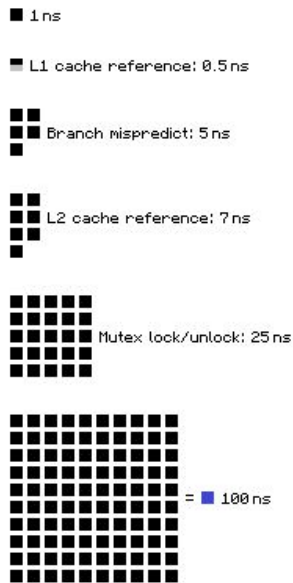
Spectre – úvod



```
if (x < A)
    tajne = adresar[x]
    cti(zaznamy[tajne])
else
    mimo_adresar()
```

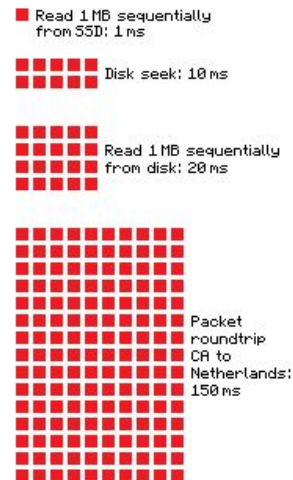
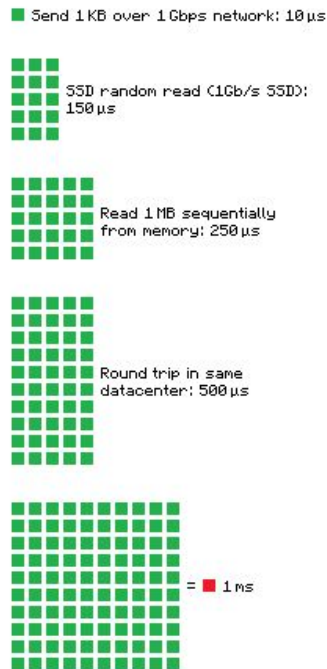
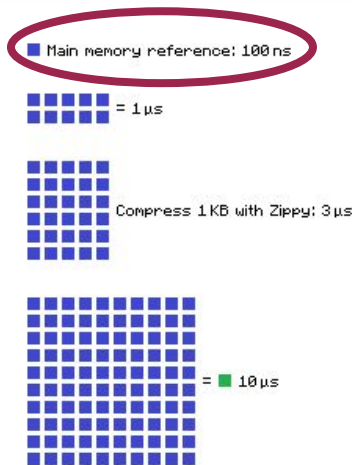
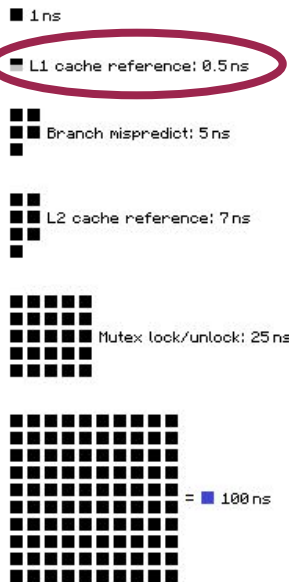
Čísla, co by měl znát každý (programátor)

Latency Numbers Every Programmer Should Know



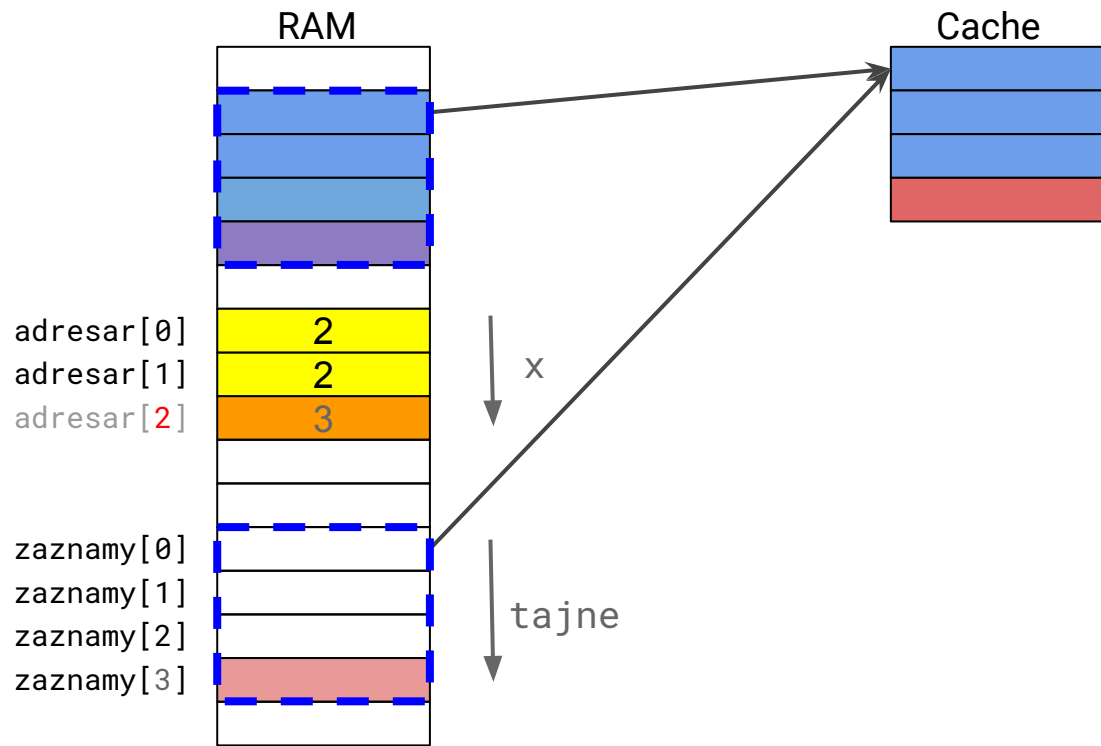
Čísla, co by měl znát každý (programátor)

Latency Numbers Every Programmer Should Know



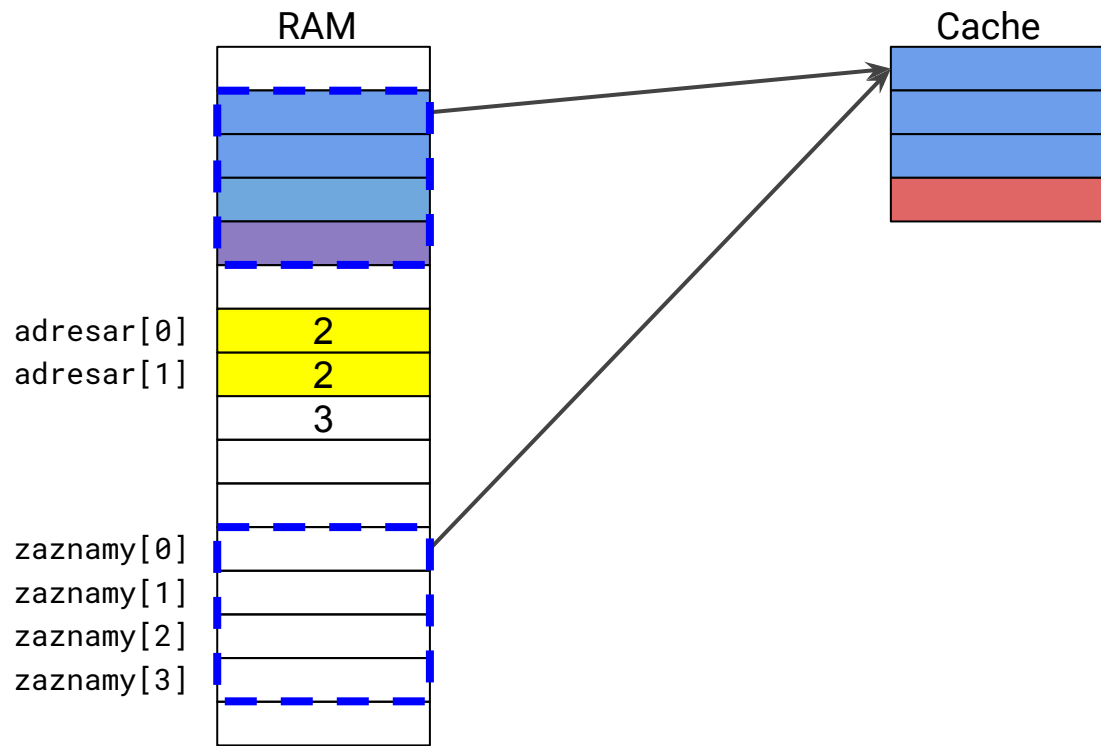
2012

Spectre



```
if (x < A)
    tajne = adresar[x]
    cti(zaznamy[tajne])
else
    mimo_adresar()
```

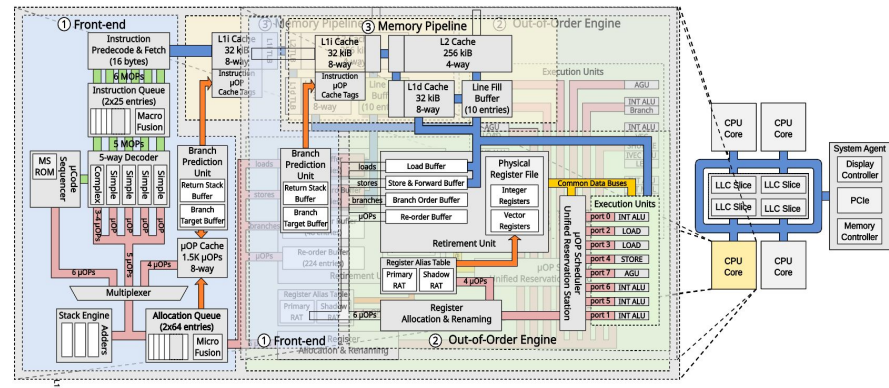
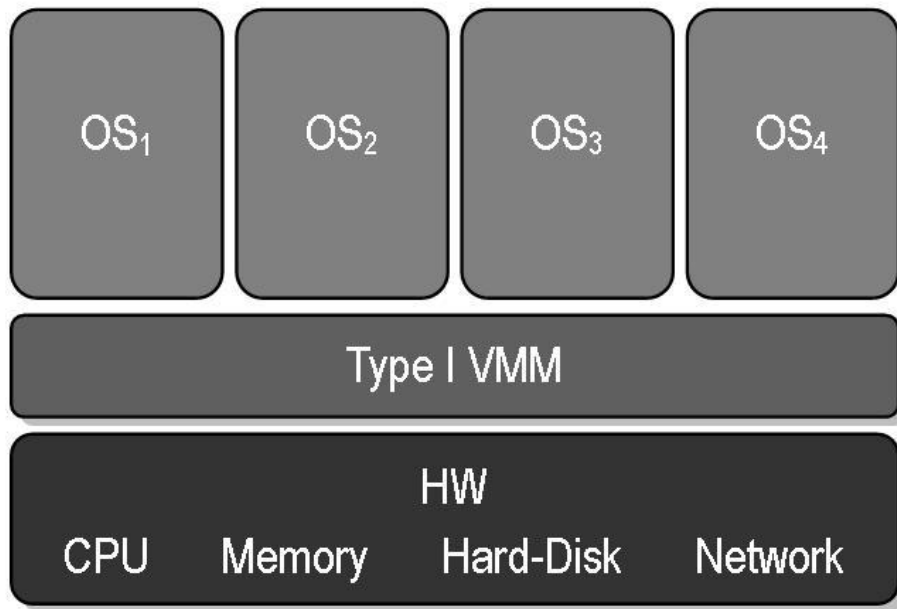
Spectre



```
if (x < A)
    tajne = adresar[x]
    cti(zaznamy[tajne])
else
    mimo_adresar()
```

Víme co

Sdílení a virtualizace



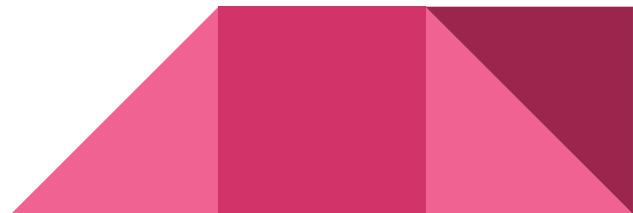
Co to znamená v praxi

Zranitelnost	Propustnost*
Meltdown	3,2–550 KB/s
Spectre	0,04–10 KB/s
Zombieload (MDS)	0,07–250 KB/s
Crosstalk	3 KB/s



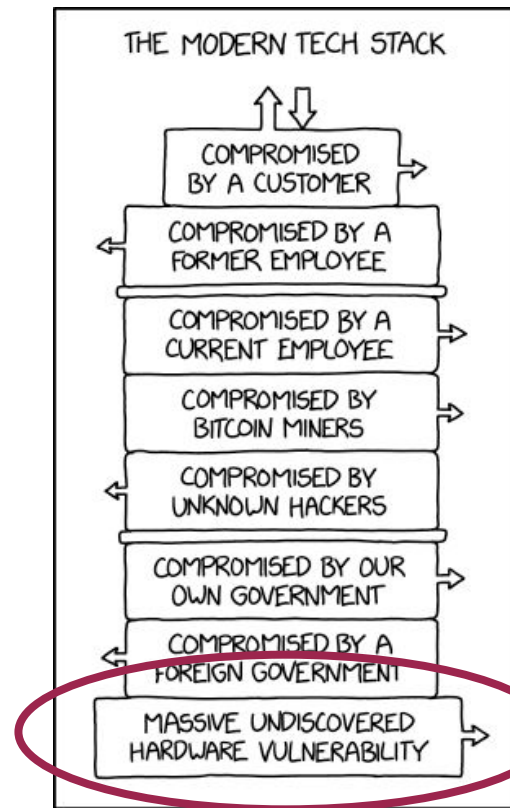
Co to znamená v praxi 2

- Nový procesor
- Nový mikrokód
- Aktualizace SW
- Vypnutí SMT (symmetric multi-threading), core scheduling



Co to znamená v praxi 2

- Nový procesor
- Nový mikrokód
- **Aktualizace SW**
- Vypnutí SMT (symmetric multi-threading), core scheduling



Média

- https://commons.wikimedia.org/wiki/File:Cern_datacenter.jpg (Hugovanmeijeren, CC BY-SA 3.0)
- https://commons.wikimedia.org/wiki/File:Acer_Predator_Helios_300_back_panel_open.jpg (Vjdeep, CC BY 3.0)
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- Public domain

Reference

- <https://spectreattack.com/spectre.pdf>
- <https://meltdownattack.com/meltdown.pdf>
- <https://mdsattacks.com/files/fallout.pdf>
- <https://mdsattacks.com/files/ridl.pdf>
- https://download.vusec.net/papers/crosstalk_sp21.pdf



Děkuji. Otázky?